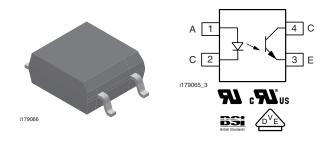


Vishay Semiconductors

Optocoupler Phototransistor Output, SOP-4, 100 mil pitch, Mini-Flat Package



DESCRIPTION

The SFH690ABT, SFH690AT, SFH690BT, SFH690CT, SFH690DT family has a GaAs infrared emitting diode emitter, which is optically coupled to a silicon planar phototransistor detector, and is incorporated in a 4 pin 100 mil lead pitch miniflat package. It features a high current transfer ratio, low coupling capacitance, and high isolation voltage.

The coupling devices are designed for signal transmission between two electrically separated circuits. The SFH690 series is available only on tape and reel. There are 2000 parts per reel. Marking for SFH690AT is 690A; SFH690BT is 690B; SFH690CT is 690C; SFH690DT is 690D; SFH690ABT will be marked as 690A or 690B.

FEATURES

- SOP (small outline package)
- Isolation test voltage, 3750 V_{RMS} (1 s)
- High collector emitter breakdown voltage, $V_{CEO} = 70 \text{ V}$
- Low saturation voltage
- · Fast switching times
- Temperature stable
- · Low coupling capacitance
- End-stackable, 0.100" (2.54 mm) spacing
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

APPLICATIONS

- · High density mounting or space sensitive PCBs
- PLCs
- Telecommunication

AGENCY APPROVALS

- UL1577, file no. E52744 system code U
- cUL tested to CSA 22.2 bulletin 5A
- BSI IEC 60950; IEC 60065
- DIN EN 60747-5-2 (VDE 0884) available with option 1

ORDERING INFORMATIO	N					
S F H	6 9	0 #	(#) T]	SOP-4	
	PART NUMBER		TAPE A REE	···-	7.21 mm	
AGENCY CERTIFIED/PACKAGE	CERTIFIED/PACKAGE CTR (%)					
UL, cUL, BSI	50 to 300	50 to 150	100 to 300	100 to 200	200 to 400	
SOP-4, 100 mil pitch	SFH690ABT	SFH690AT	SFH690BT	SFH690CT	SFH690DT	

ABSOLUTE MAXIMUM RATINGS ⁽¹⁾ ($T_{amb} = 25$ °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	VALUE	UNIT				
INPUT							
Reverse voltage		V _R	6	V			
DC forward current		I _F	50	mA			
Surge forward current	t _p ≤ 10 μs	I _{FSM}	2.5	А			
Power dissipation		P _{diss}	80	mW			
OUTPUT							
Collector emitter voltage		V _{CEO}	70	V			
Emitter collector voltage		V _{ECO}	7	V			
Collector current		Ι _C	50	mA			
Collector current	t _p ≤ 1 ms	Ι _C	100	mA			
Power dissipation		P _{diss}	150	mW			





COMPLIANT



Vishay Semiconductors Optocoupler Phototransistor Output, SOP-4, 100 mil pitch, Mini-Flat Package

ABSOLUTE MAXIMUM RATINGS (1) (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT				
COUPLER								
Isolation test voltage between emitter and detector (1 s)		V _{ISO}	3750	V _{RMS}				
Isolation resistance	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 25 ^{\circ}\text{C}$	R _{IO}	≥ 10 ¹²	Ω				
Isolation resistance	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 100 ^{\circ}\text{C}$	R _{IO}	≥ 10 ¹¹	Ω				
Storage temperature range		T _{stg}	- 55 to + 150	°C				
Ambient temperature range		T _{amb}	- 55 to + 100	°C				
Soldering temperature ⁽²⁾	max. 10 s dip soldering distance to seating plane ≥ 1.5 mm	T _{sld}	260	°C				

Notes

(1) Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

⁽²⁾ Refer to reflow profile for soldering conditions for surface mounted devices.

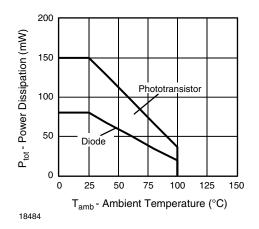


Fig. 1 - Permissible Power Dissipation vs. Ambient Temperature

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)									
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT			
INPUT									
Forward voltage	I _F = 5 mA	V _F		1.15	1.4	V			
Reverse current	V _R = 6 V	I _R		0.01	10	μA			
Capacitance	$V_R = 0 V, f = 1 MHz$	C _O		14		pF			
Thermal resistance		R _{thJA}		750		K/W			
OUTPUT									
Collector emitter leakage current	V _{CE} = 20 V	I _{CEO}			100	nA			
Collector emitter capacitance	V _{CE} = 5 V, f = 1 MHz	C _{CE}		2.8		pF			
Thermal resistance		R _{thJA}		500		K/W			
COUPLER									
Collector emitter saturation voltage	$I_{\rm F} = 10$ mA, $I_{\rm C} = 2$ mA	V _{CEsat}		0.1	0.3	V			
Coupling capacitance	f = 1 MHz	C _C		0.3		pF			

Note

 Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.



Optocoupler Phototransistor Output, SOP-4, Vishay Semiconductors 100 mil pitch, Mini-Flat Package

CURRENT TRANSFER RATIO									
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT		
	I _F = 5 mA, V _{CE} = 5 V	SFH690ABT	CTR	50		300	%		
		SFH690AT	CTR	50		150	%		
I _C /I _F		SFH690BT	CTR	100		300	%		
		SFH690CT	CTR	100		200	%		
		SFH690DT	CTR	200		400	%		

SWITCHING CHARACTERISTICS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Rise time	I_C = 2 mA, V_{CC} = 5 V, R_L = 100 Ω	tr		3		μs	
Fall time	I_C = 2 mA, V_{CC} = 5 V, R_L = 100 Ω	t _f		4		μs	
Turn-on time	I_{C} = 2 mA, V_{CC} = 5 V, R_{L} = 100 Ω	t _{on}		5		μs	
Turn-off time	I_C = 2 mA, V_{CC} = 5 V, R_L = 100 Ω	t _{off}		3		μs	

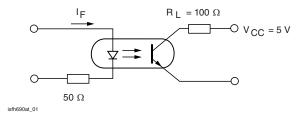


Fig. 2 - Switching Operation (without Saturation)

SAFETY AND INSULATION RATINGS									
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT			
Climatic classification (according to IEC 68 part 1)				55/100/21					
Comparative tracking index		CTI	175		399				
V _{IOTM}			6000			V			
V _{IORM}			707			V			
P _{SO}					350	mW			
I _{SI}					150	mA			
T _{SI}					175	°C			
Creepage distance			5			mm			
Clearance distance			5			mm			
Insulation thickness			0.4			mm			

Note

• As per IEC 60747-5-5, §7.4.3.8.1, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.



Vishay Semiconductors Optocoupler Phototransistor Output, SOP-4, 100 mil pitch, Mini-Flat Package

TYPICAL CHARACTERISTICS ($T_{amb} = 25 \text{ °C}$, unless otherwise specified)

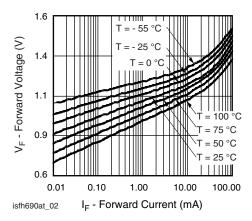


Fig. 3 - Diode Forward Voltage vs. Forward Current

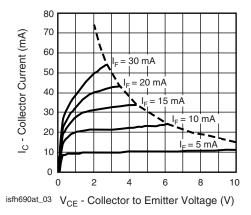
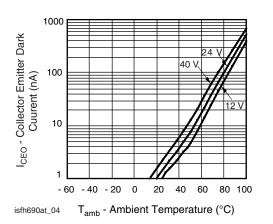


Fig. 4 - Collector Current vs. Collector Emitter Voltage





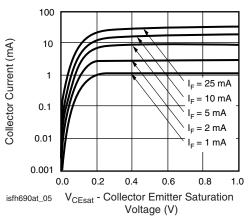


Fig. 6 - Collector Current vs. Collector Emitter Saturation Voltage

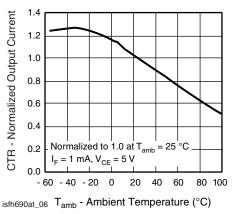


Fig. 7 - Normalized Output Current vs. Ambient Temperature

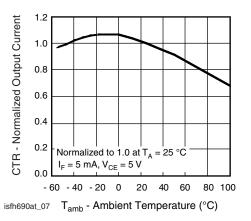


Fig. 8 - Normalized Output Current vs. Ambient Temperature



Optocoupler Phototransistor Output, SOP-4, Vishay Semiconductors 100 mil pitch, Mini-Flat Package

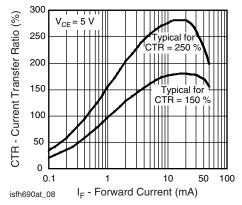


Fig. 9 - Current Transfer Ratio vs. Forward Current

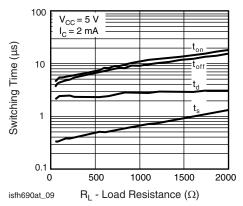


Fig. 10 - Switching Time vs. Load Resistance

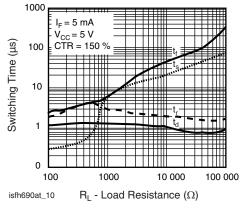
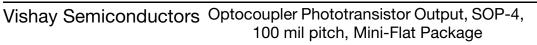
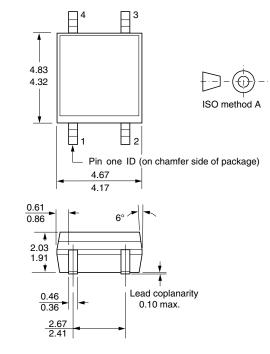


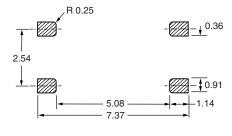
Fig. 11 - Switching Time vs. Load Resistance

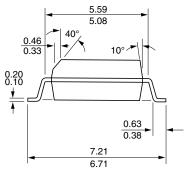




PACKAGE DIMENSIONS in millimeters







i178037

PACKAGE MARKING (example of SFH690AT)





Vishay

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